Silicon Interfaces - *a software and vlsi design center* - is organizing an Overlay Institute, to develop *Intelligent and Dedicated VLSI Engineers* as per the Requirements of the Semiconductor Industry, under the franchise name of **SILICON INSTITUTE OF TECHNOLOGY**.

Features

- Initiated by Company with 16+ years in VLSI Industry
- The Institute has alliances with international Universities
- Affiliation to EDA Vendor University program ongoing
- **Faculty** includes, MS, USA, Qualified, Trained and Internationally Experienced in Semiconductor Designs
- The Institute is Based in the Commercial, Business and Residential areas of Mumbai, close to major stations of Mumbai – Churchgate and CST

Program

- 4-month Advanced VLSI program

- Program includes Curriculum (designed in conjunction with *Silicon Interfaces* and its requirements) with multi-modules:
  - Logic Design:
    - Combinational / Sequential (Sync/AsynMem/Clk/FS),
    - Memory, System Design (Block/Flow Charts/Meth),
    - Verification,Top Down Design Methodologies, Documentation.

  - HDL:
    - HDL & HVL (Test Bench) - Verilog /Advanced Verilog
    - Advanced - VHDL, Advanced - PLI/Compiler Directives

  - Methodologies & Advanced Verification:
    - Methodologies IP, FPGA, ASIC & SOC
    - VDL - Advanced - SystemVerilog
    - Advanced Verification Methodologies - OVM

Program

- **Verticals**
  - Hosts - PCI, Data Communications - USB
  - Processor Verification

- **Backend**
  - Backend - Static Timing Analysis
  - Backend - Semi-custom - STA
  - Backend - Full Custom

- **Environments**
  - Linux (Basics/Commands)
  - Scripting (Shell/Perl),
  - EDA flow (Front-end/Backend)
  - Install - Linux, Tools.
  - EDA tools (Practical)
  - Lab - Frontend Design & Verification
  - Mini Project
  - Paper Writing/Presentation
  - Lab VIP
  - Paper Writing/Presentation
  - Lab Semi Custom

Program

- After every month there will be Test, Check Point Results & Counseling, finally in the last month FINAL Results(!) of all.
- State-of-the-art training facilities with hardware and software equipment and necessary EDA tools
- A mix of theory + Lab projects makes the student understand concepts and then apply.
- Course materials created based on real-world experience + Research in US universities.

Eligibility Criteria

- Should be a BE (Electronic, Computer, Communications, Telecommunication, Electrical and Instrumentation) with degree cut off year at least 3 years prior
- B.Sc / Diploma (Physics, Computer Science, Electronics) with cut-off year atleast 4 years prior

Selection Process for **SIT**

- Attendance of Presentation On Program.
- Passing written Entrance Test on Problem Solving and Analytical.
- Passing the Communications Skills Group Discussion or Stand-up Topical Talk
- General Interview.